



Spec Sheet LPC1343

Provided Software

A suite of software is provided for use with the RS-EDP platform. This software includes all the low level driver to talk to the other modules including Analogue Module AN16, Digital IO Module DIO54, Communications Module CO1, Brushed DC Motor Drive Module MC1.

The software has been written using Keil uVision MDK-ARM and fits inside the 32k code limit evaluation version of the tool.

Key Features

- 48pin Cortex-M3 NXP LPC1343 CPU device capable of running up to 72MHz
- 32k bytes on board FLASH memory
- 8k bytes on board SRAM
- Full Speed USB2.0 device controller
- One UART
- One SPI interface
- One enhanced I2C interface
- 10bit on board ADC controller
- 2 x 32bit general purpose timers with PWM & capture compare capability
- 2 x 16bit general purpose timers with PWM & capture compare capability
- Watchdog Timer
- SWD (Serial Wire Debug) and Serial Wire Trace (SWT) capability
- 3.3V operation

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rswww.com/edp

